

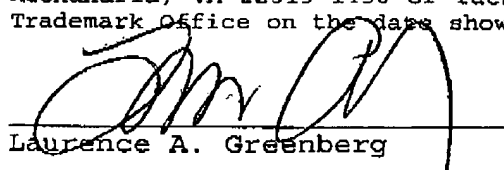
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Practitioner's Docket No.: P2002,0618

DEC 31 2005

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Laurence A. GreenbergDecember 31, 2005
DateIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/623,843 Confirmation No.: 6526
Applicants : Veronika Polei, et al.
Filed : July 21, 2003
Title : METHOD FOR FABRICATING A BURIED BIT LINE
FOR A SEMICONDUCTOR MEMORY
Art Unit : 2823
Examiner : Khiem D. Nguyen
Customer No. : 24131

RESPONSE under 37 CFR § 1.116

Mail Stop Non Fee Amendment
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S i r :

Responsive to the Office action dated October 31, 2005,
kindly consider the following remarks:

Remarks/Arguments begin on page 2 of this paper.